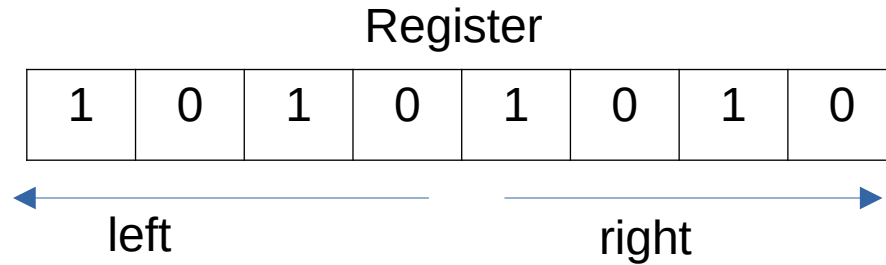


Bit shifting operations



Left side bit shifting

Assembly instruction:

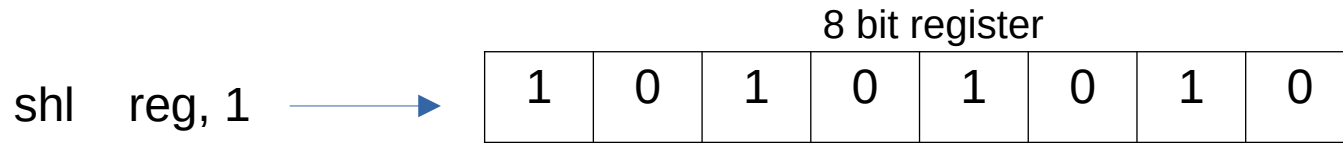
shl reg, <number of bits to shift>

Left side bit shifting

Assembly instruction:

shl reg, <number of bits to shift>

Example :

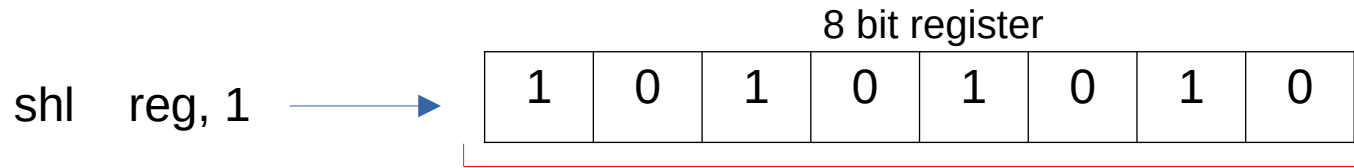


Left side bit shifting

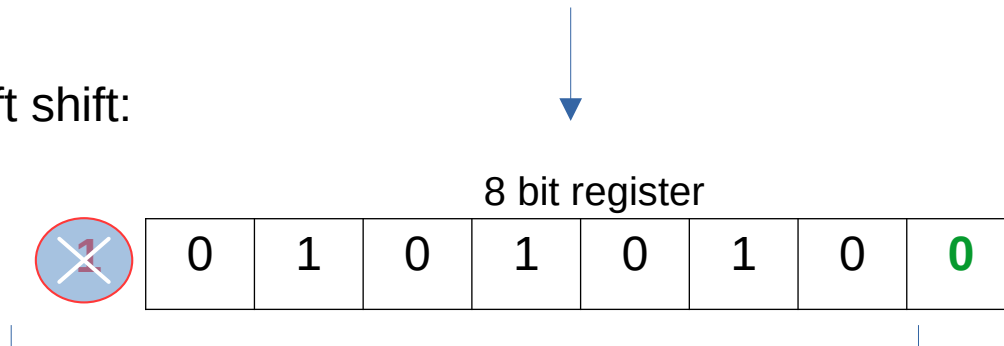
Assembly instruction:

shl reg, <number of bits to shift>

Example :



After 1 bit left shift:



Right side bit shifting

Assembly instruction:

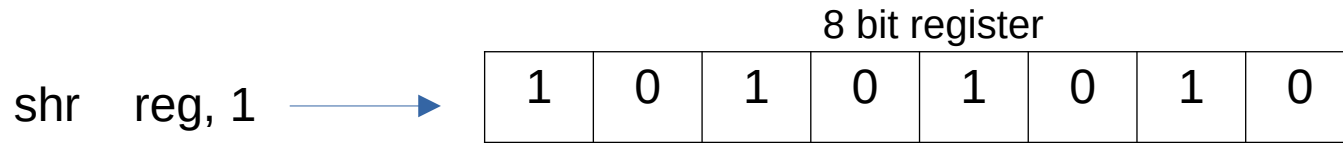
shr reg, <number of bits to shift>

Right side bit shifting

Assembly instruction:

shr reg, <number of bits to shift>

Example :



Right side bit shifting

Assembly instruction:

shr reg, <number of bits to shift>

Example :

